

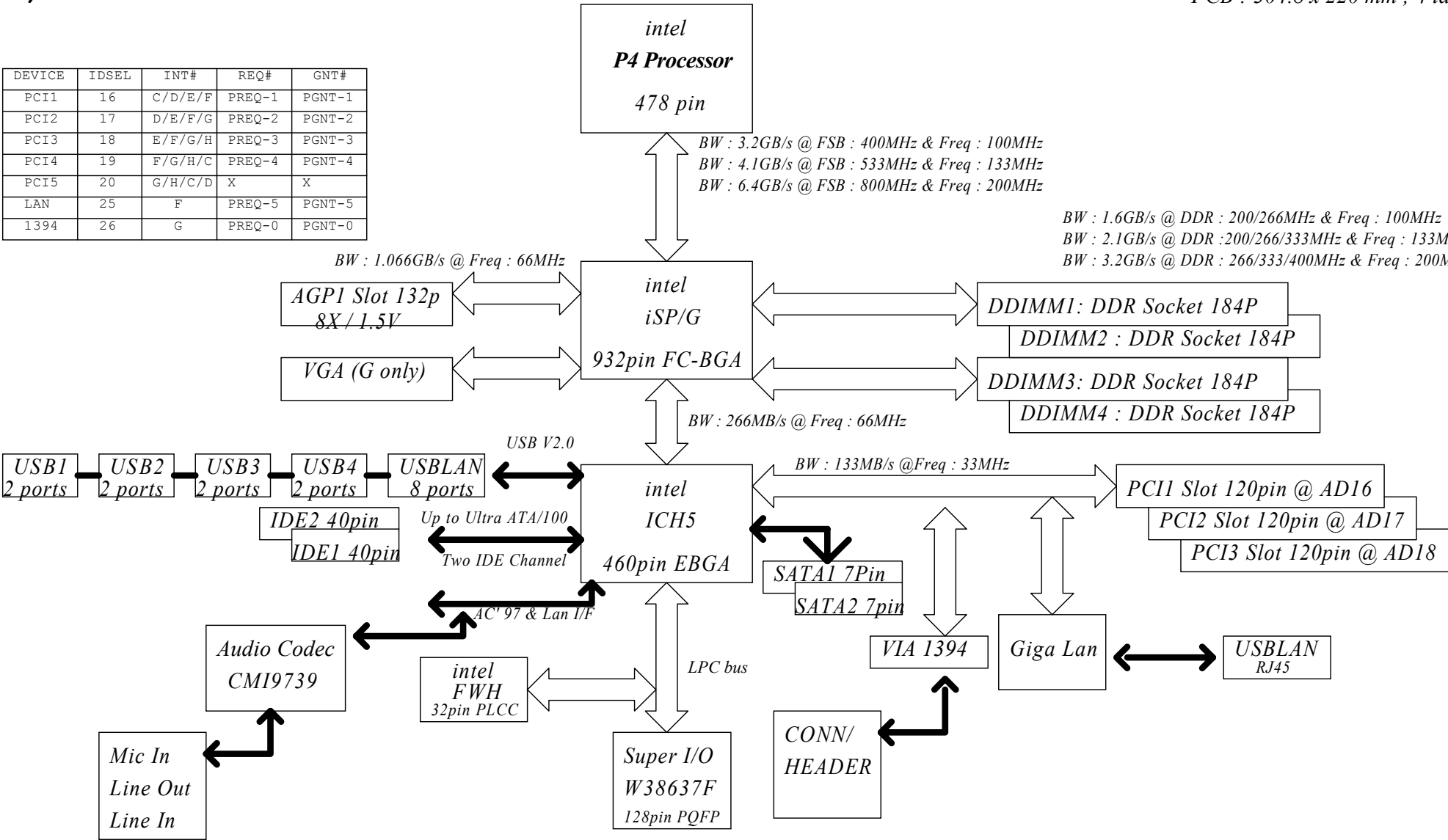
Schematics Version History Table :

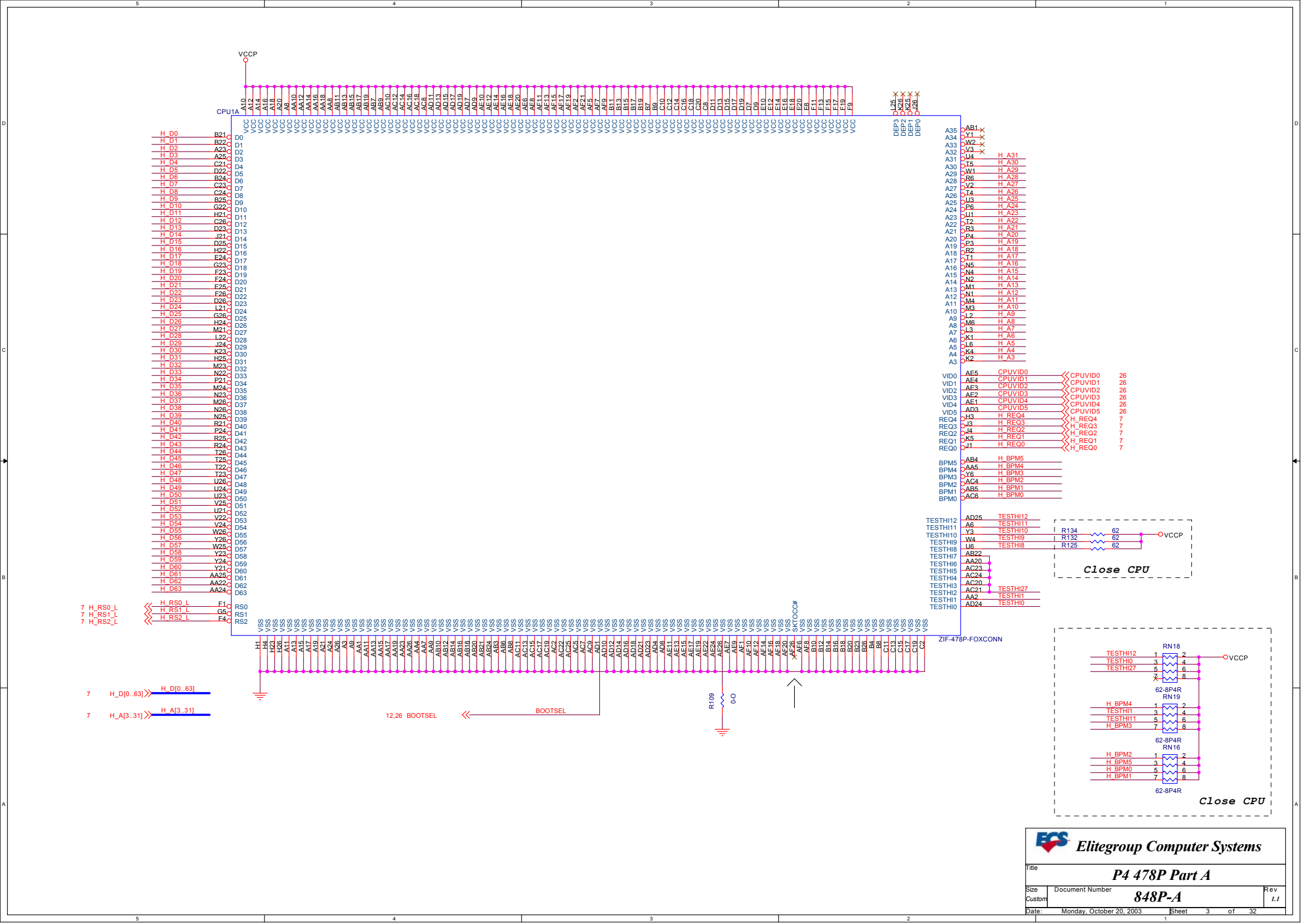
Circuit Ver.	PCB Ver.	Total Page	Modificatory Page	Date
A	A	32		5 /26 / ' 03
1.0a	1.0a	32		

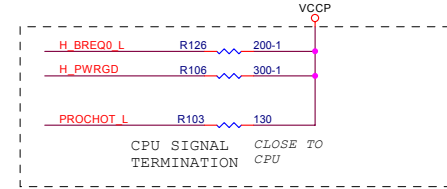
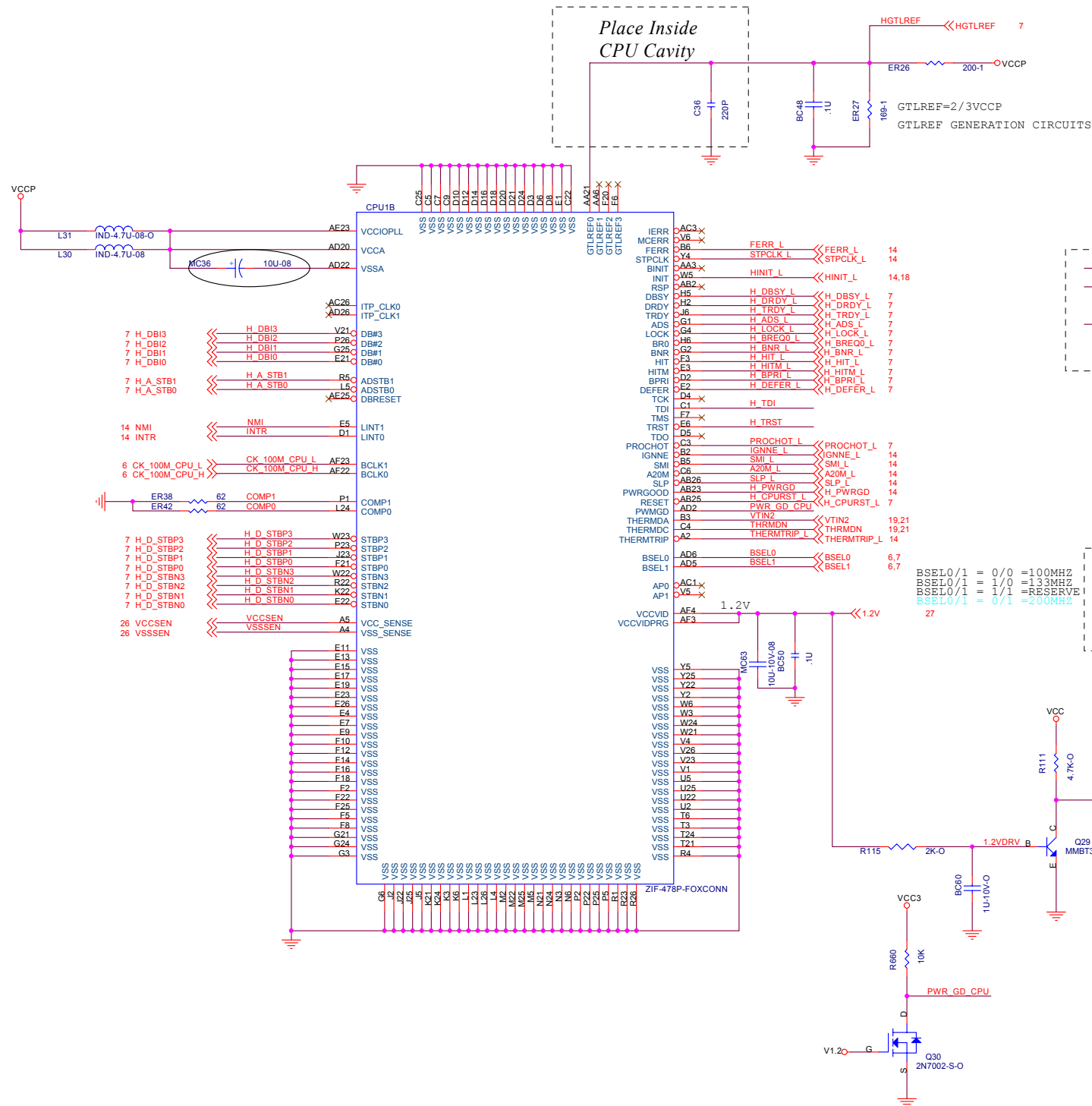
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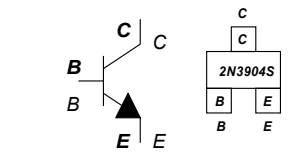
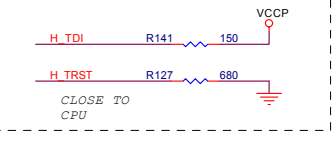
DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	16	C/D/E/F	PREQ-1	PGNT-1
PCI2	17	D/E/F/G	PREQ-2	PGNT-2
PCI3	18	E/F/G/H	PREQ-3	PGNT-3
PCI4	19	F/G/H/C	PREQ-4	PGNT-4
PCI5	20	G/H/C/D	X	X
LAN	25	F	PREQ-5	PGNT-5
1394	26	G	PREQ-0	PGNT-0

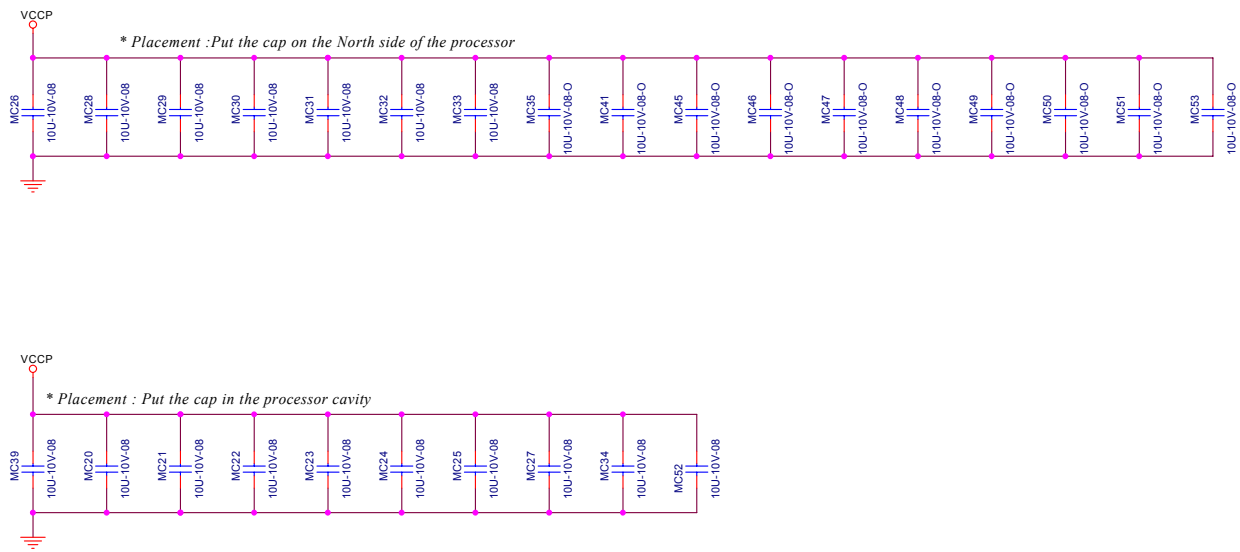


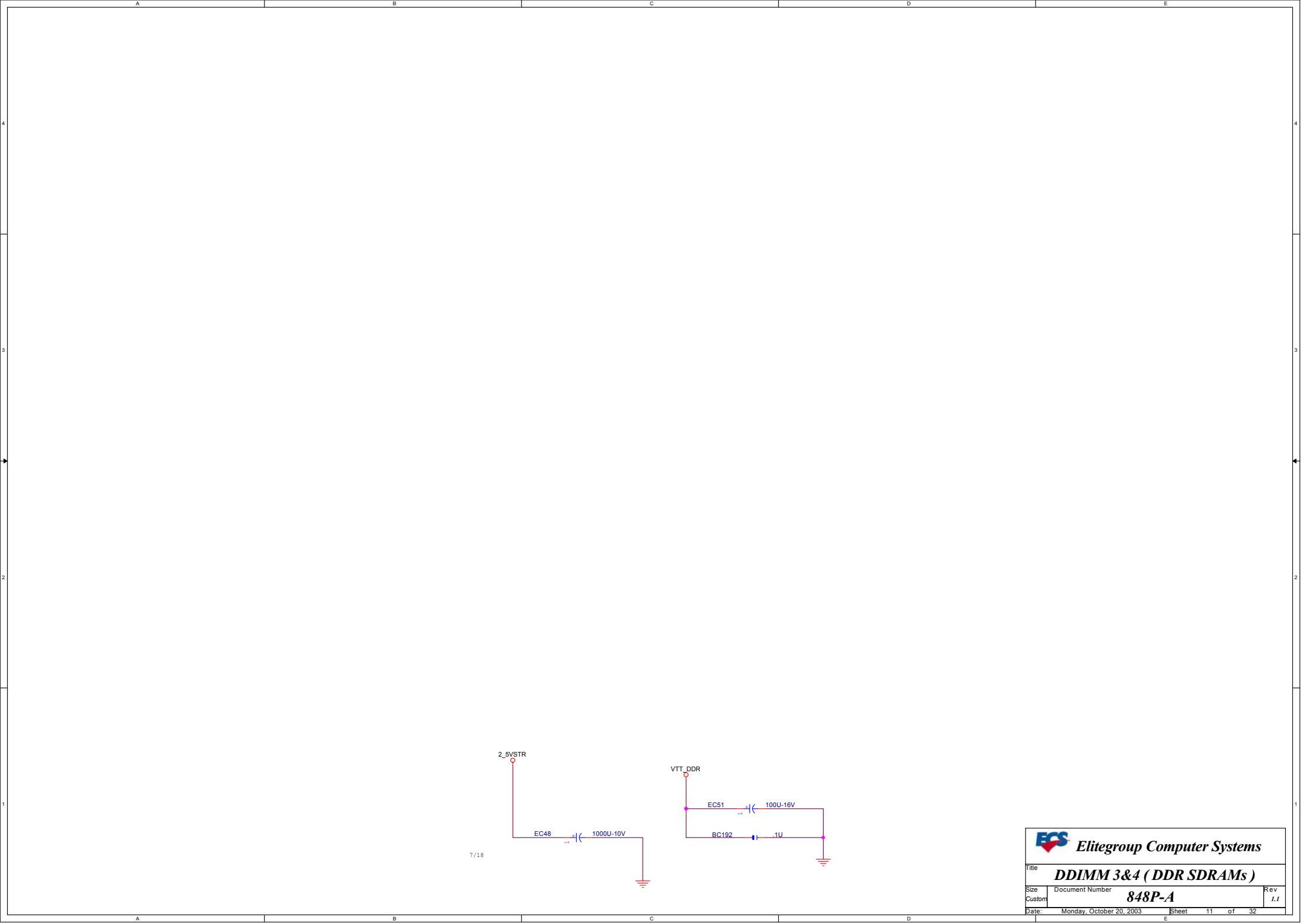




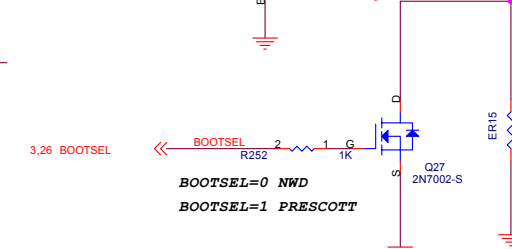
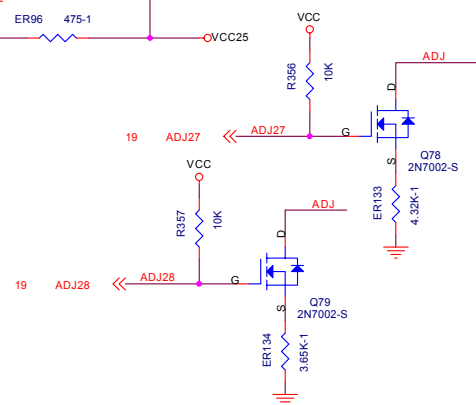
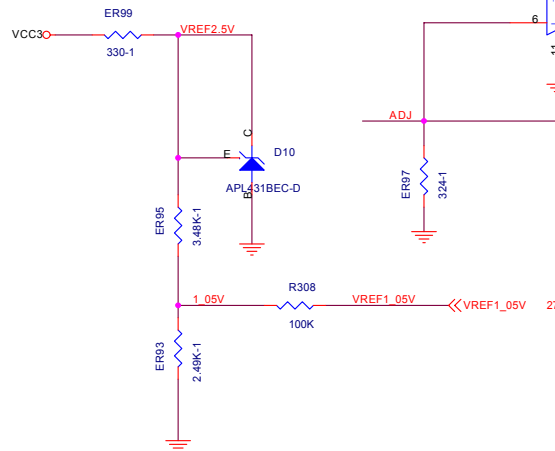
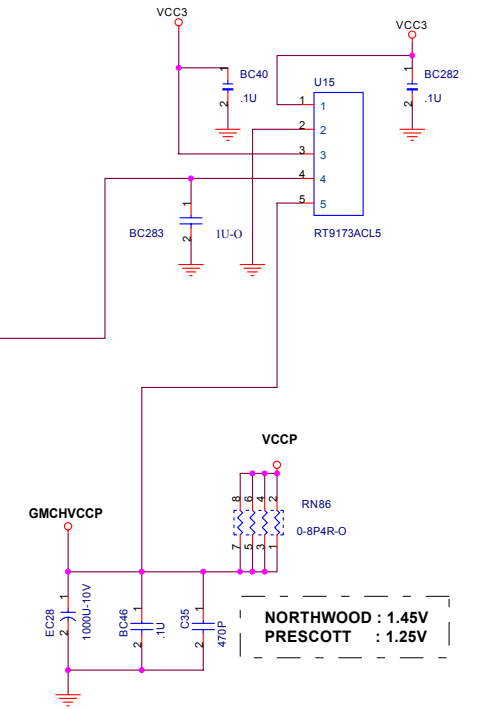
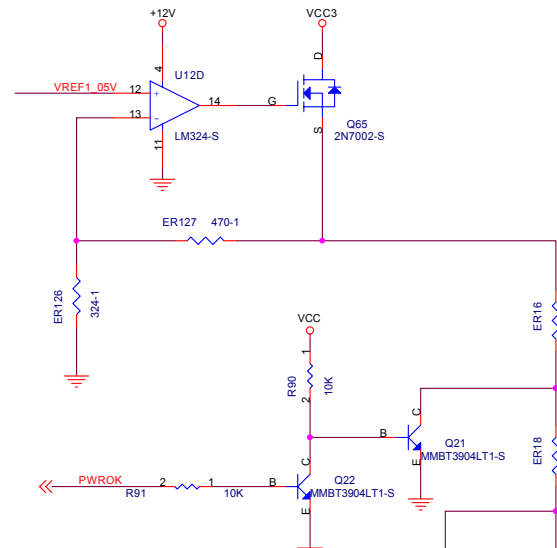
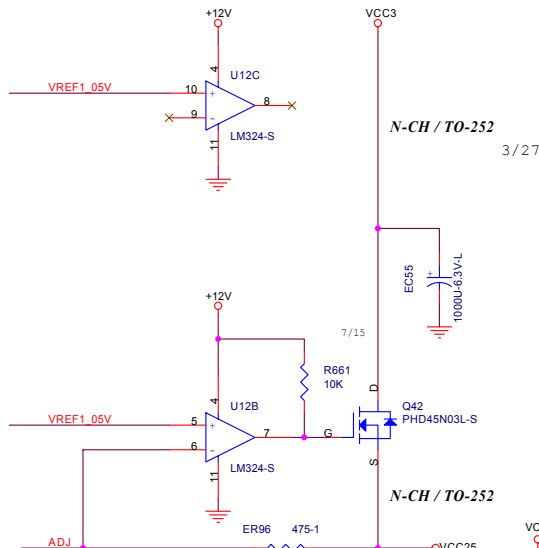
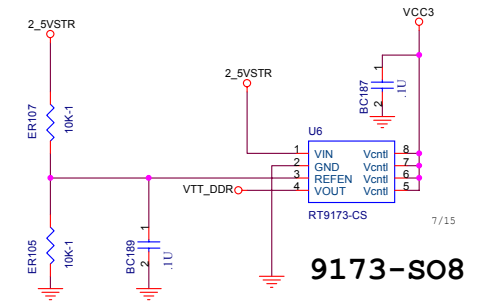
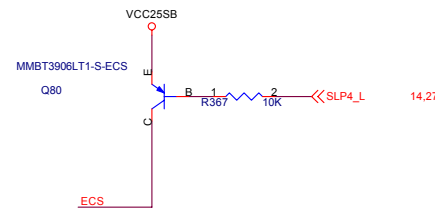
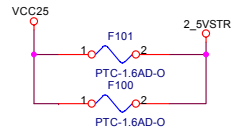
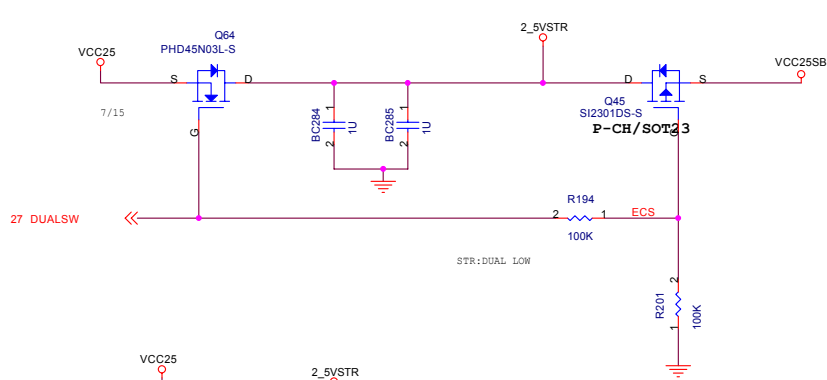
$BSEL0/1 = 0/0 = 100\text{MHZ}$
 $BSEL0/1 = 1/0 = 133\text{MHZ}$
 $BSEL0/1 = 1/1 = \text{RESERVE}$
 $BSEL0/1 = 0/1 = 200\text{MHZ}$

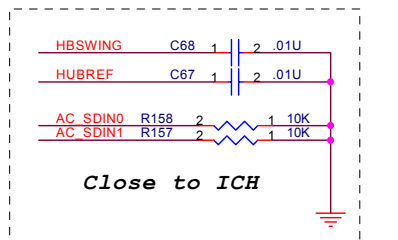
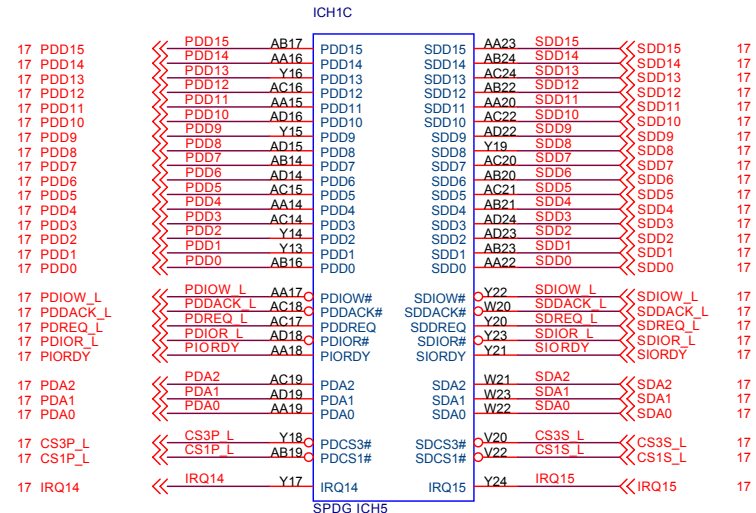
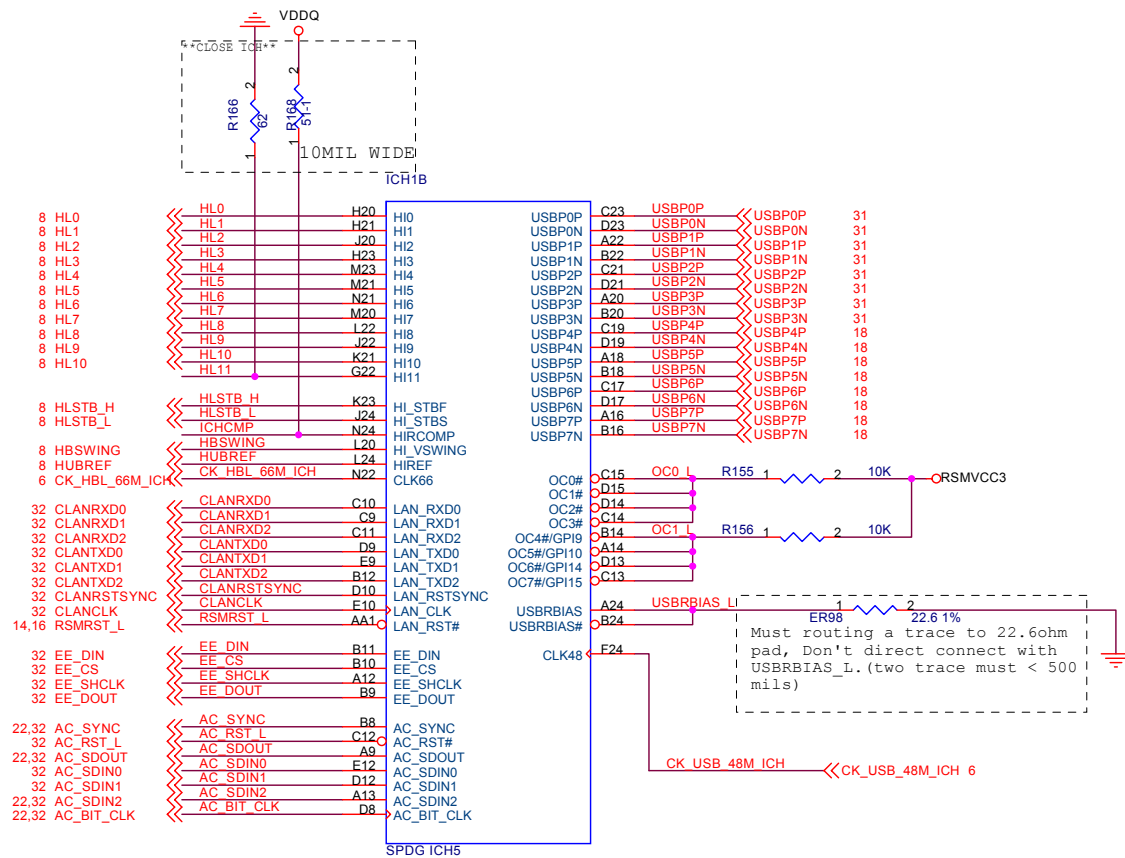


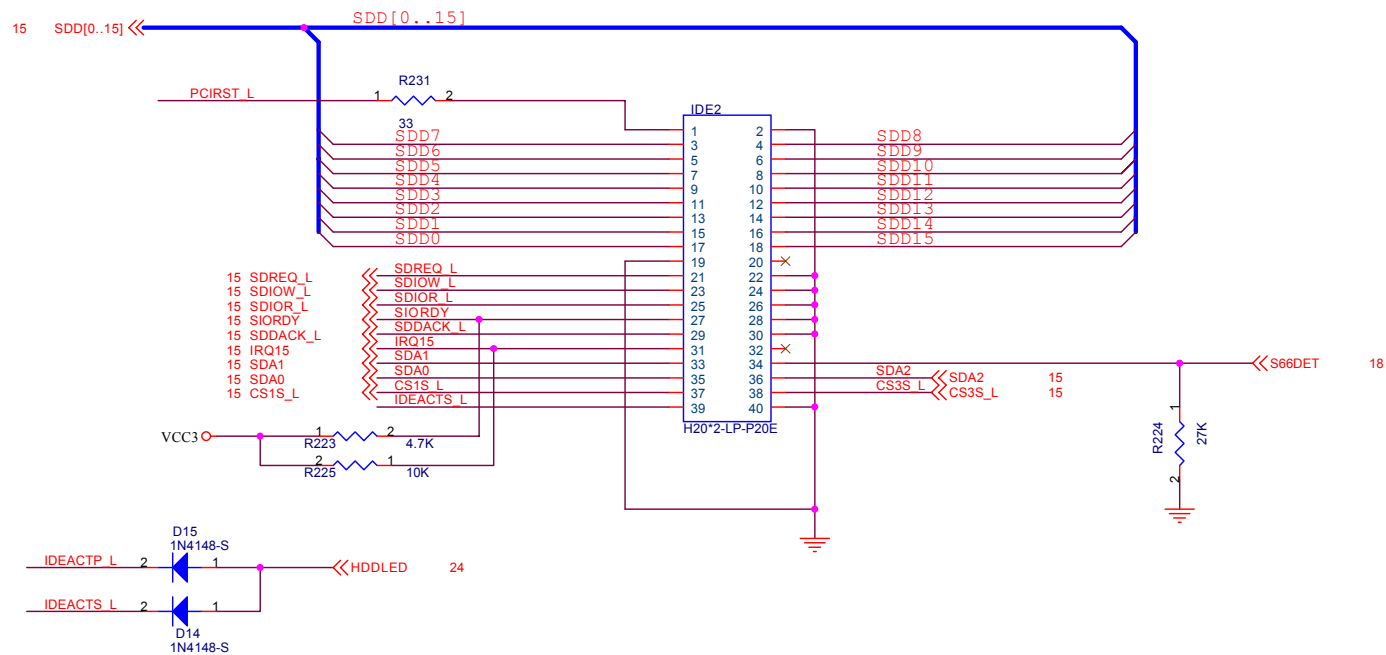
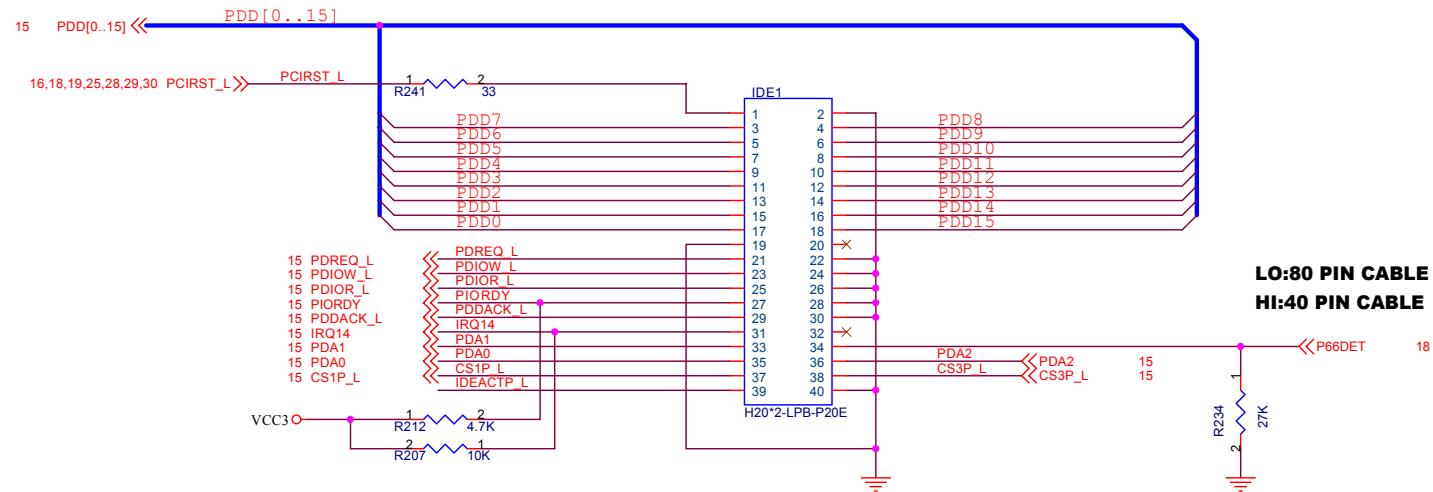




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MAX TRACE LENGTH IS 8"

100 Mils Width

USB3

USB4

FWH

GPIO

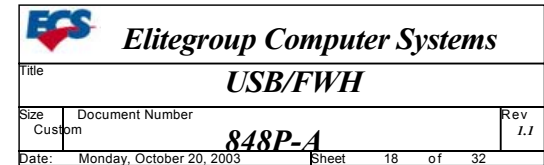
PLCC-32PBR

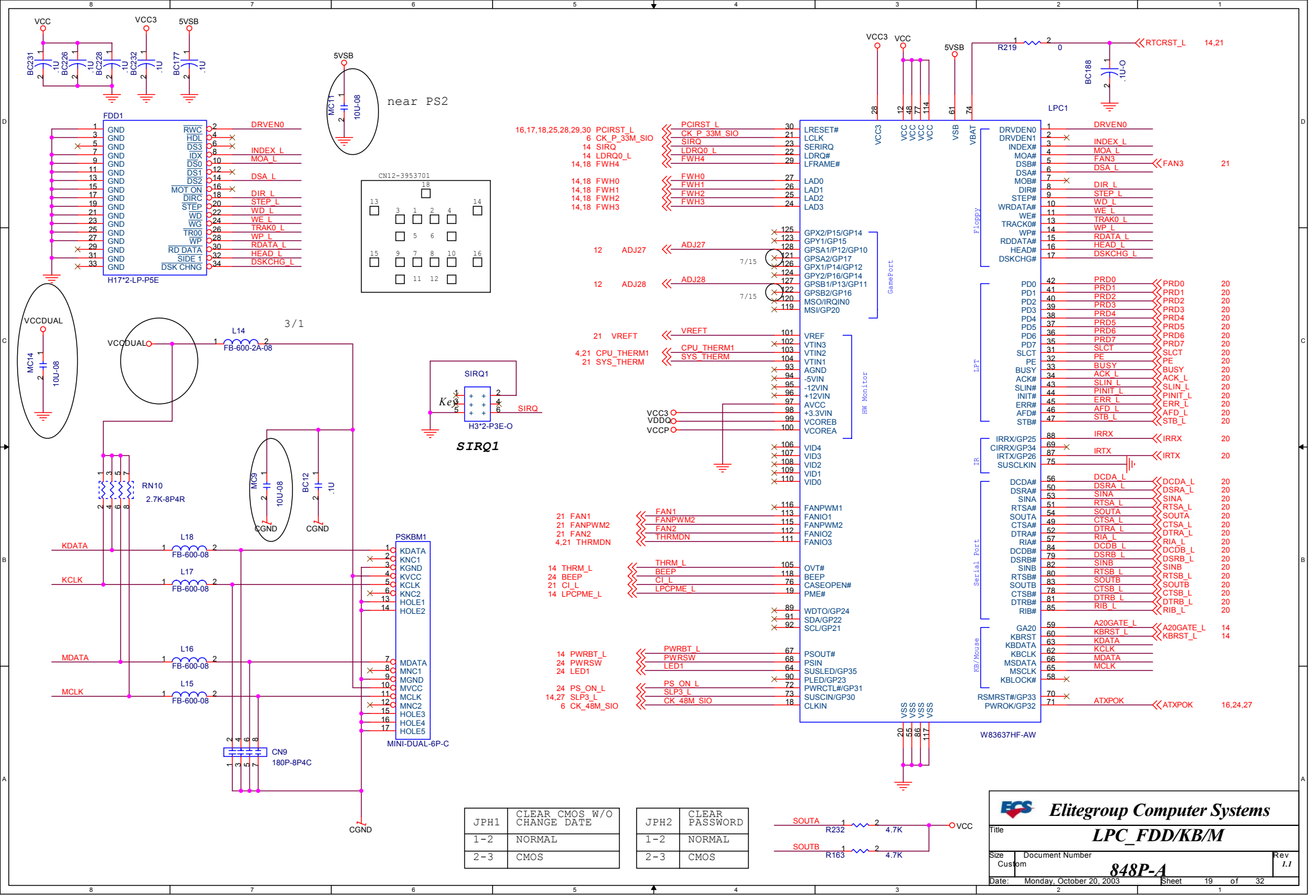
MMBT3904LT1-S

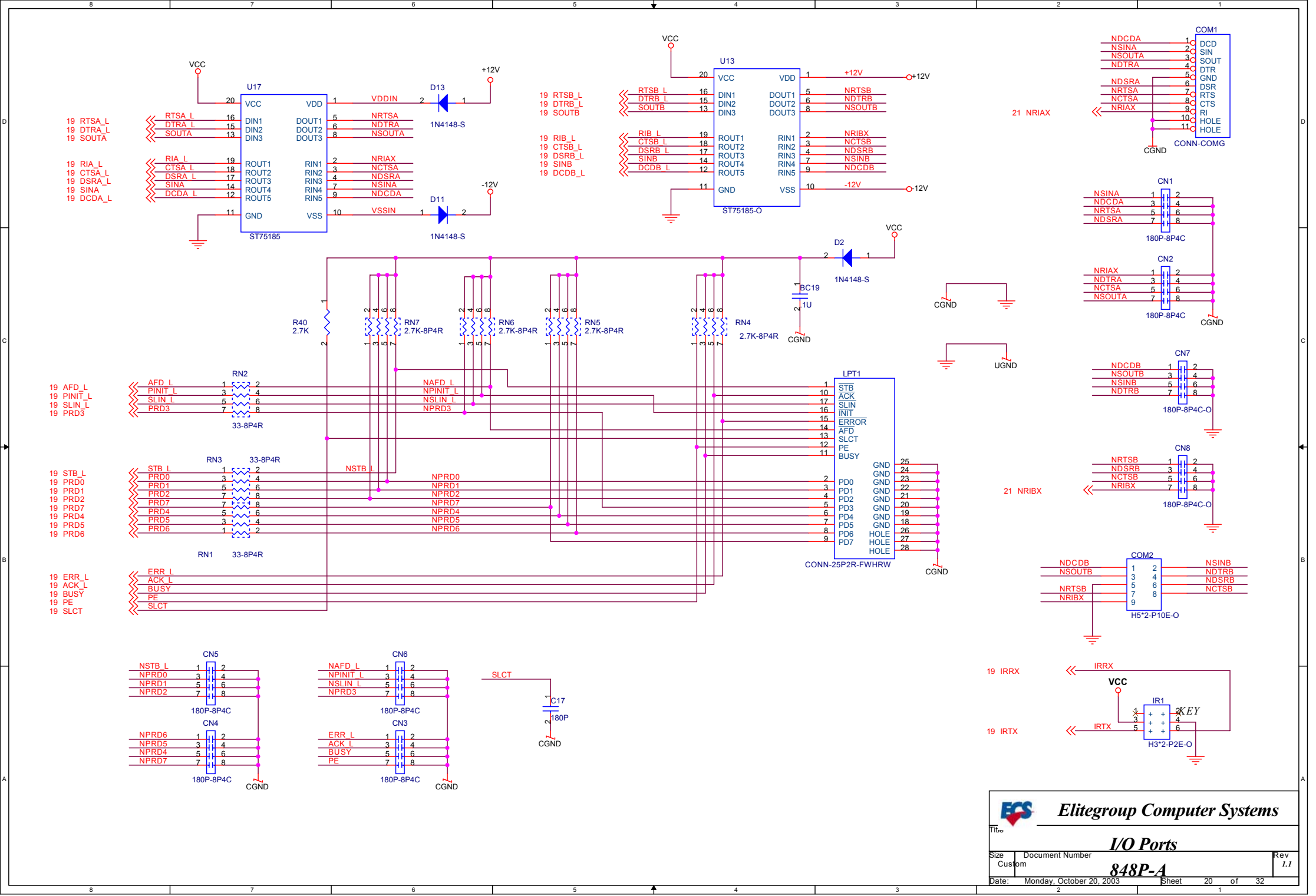
2N3904S

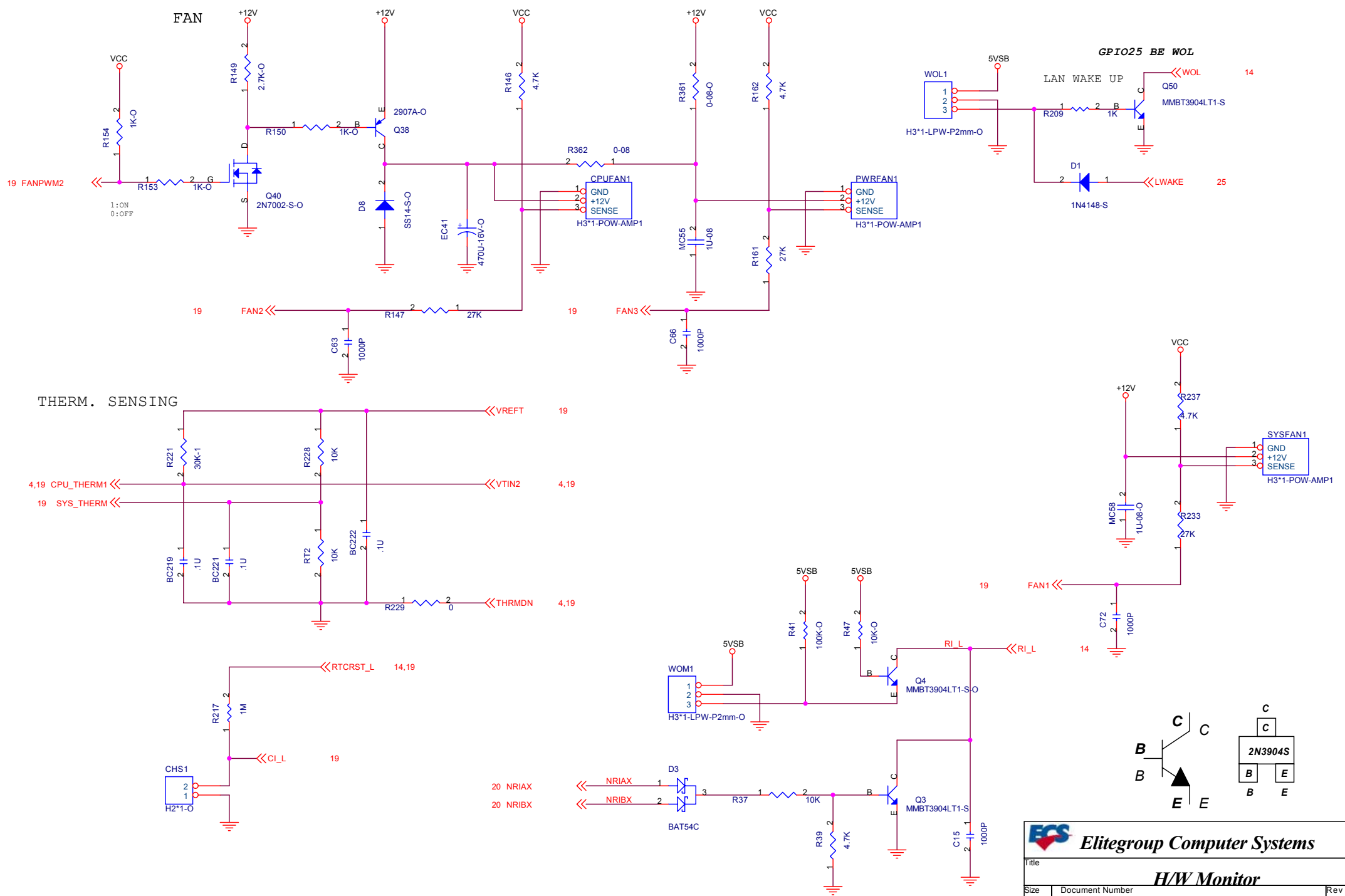
Elitegroup Computer Systems

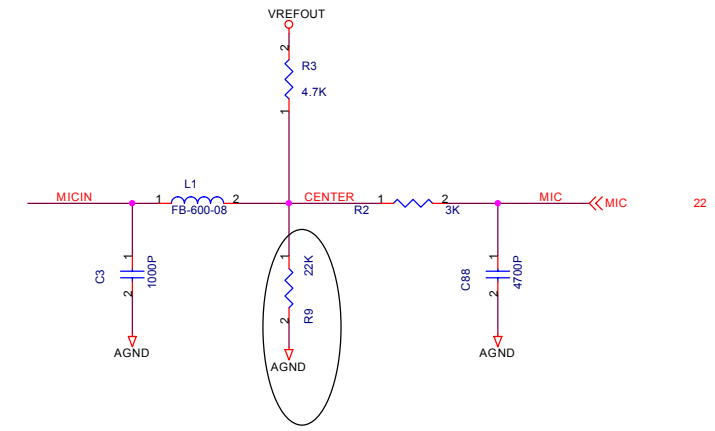
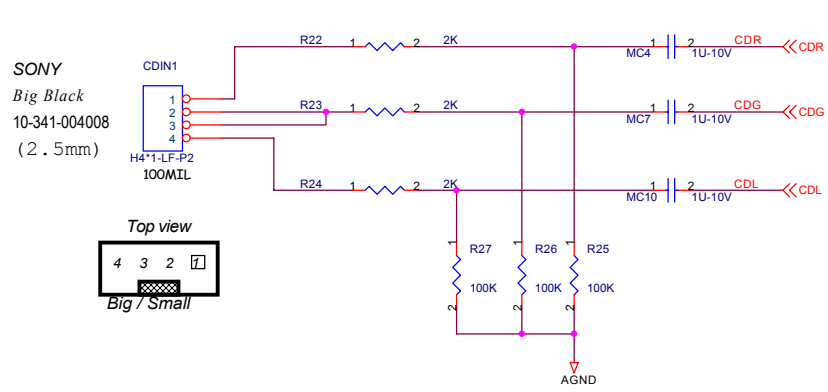
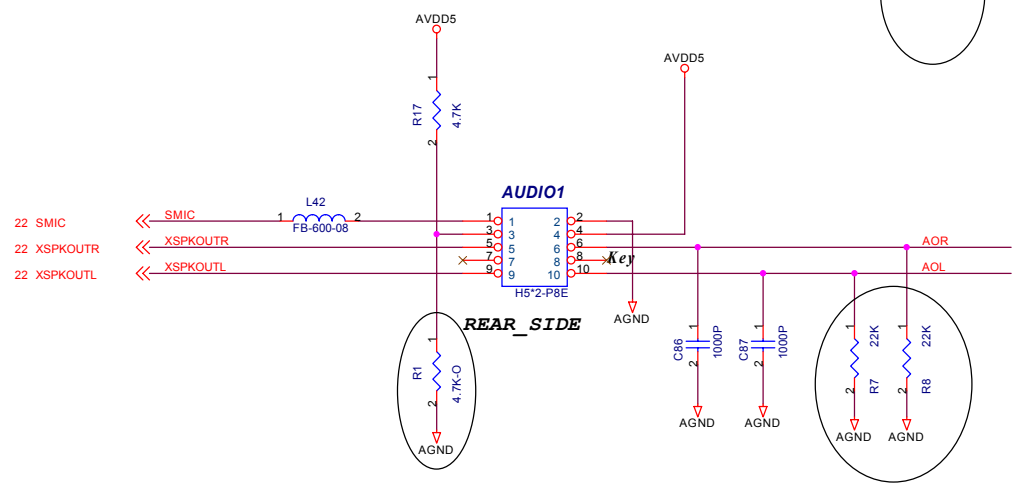
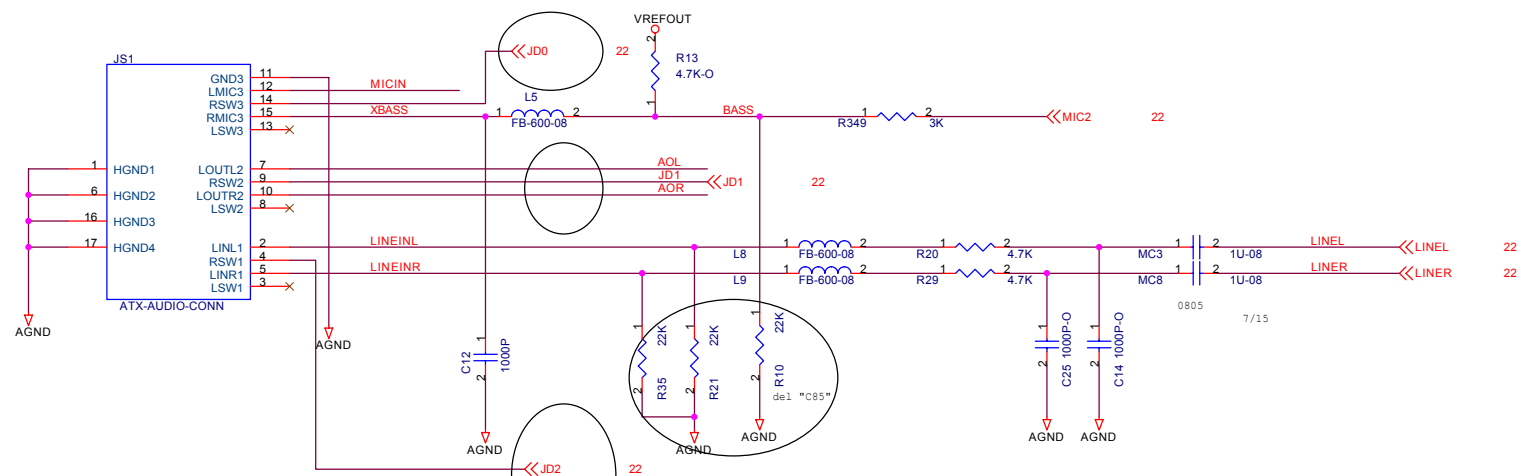
USB/FWH

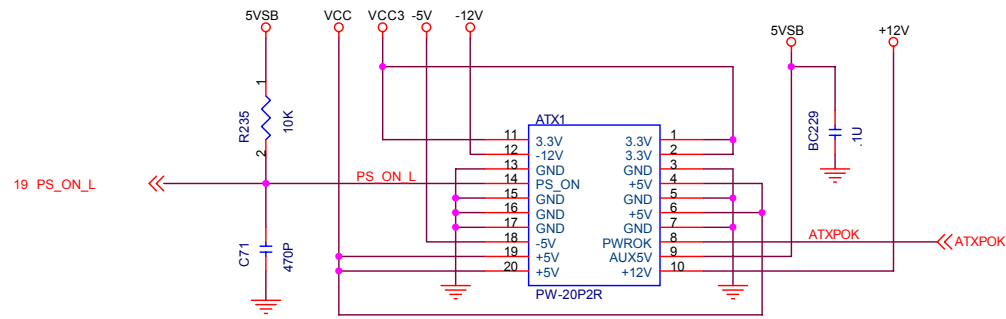




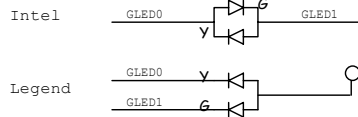
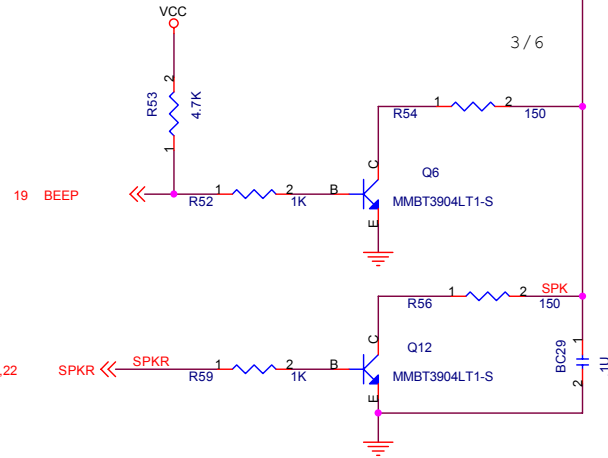
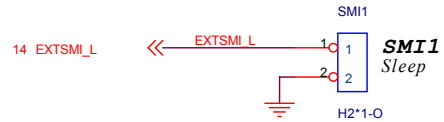
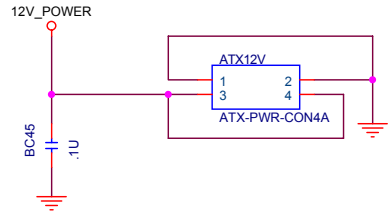
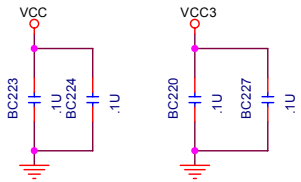




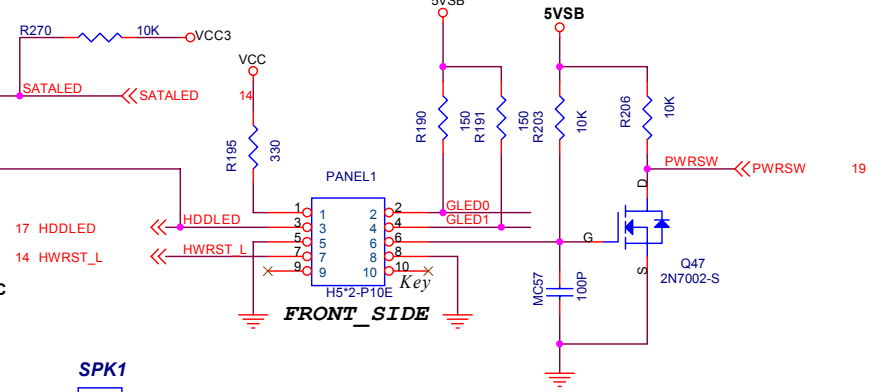
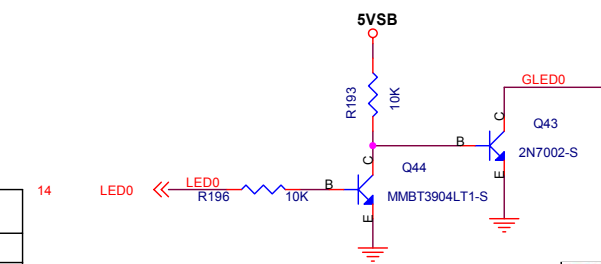
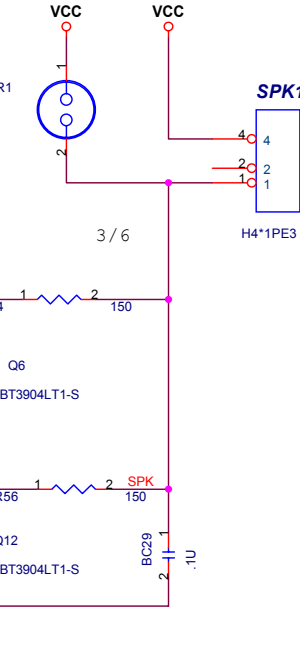
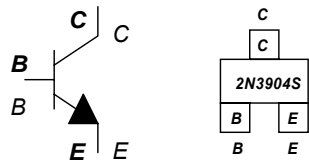




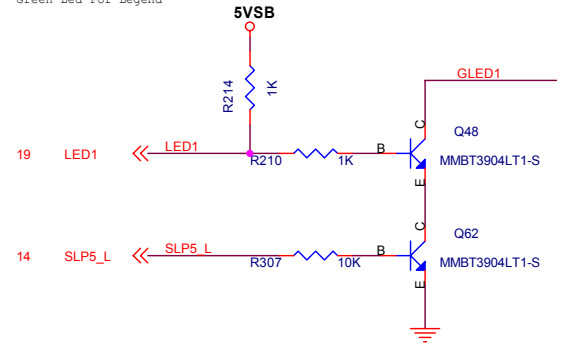
PWR CONN CAPS

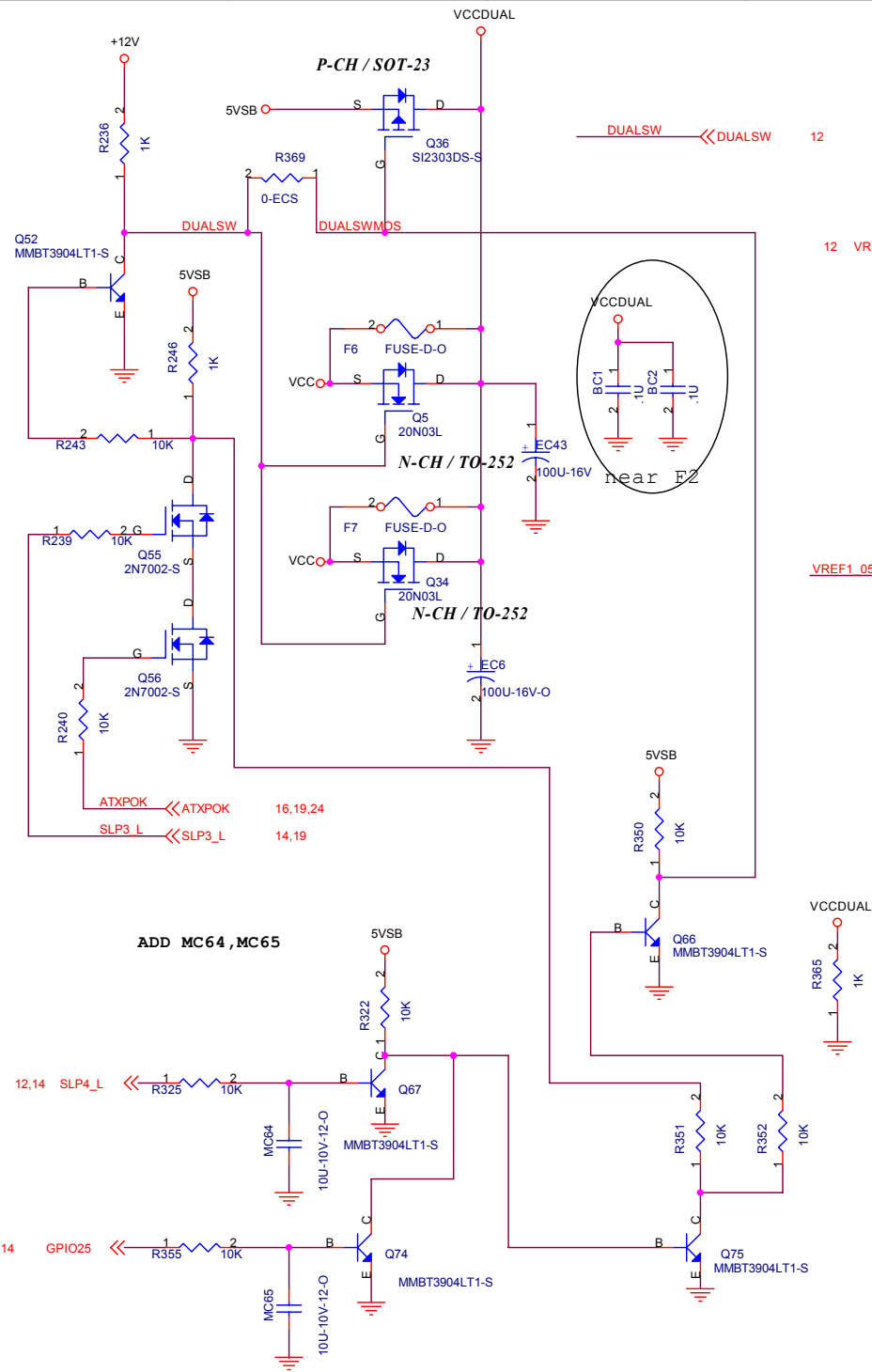


	S0	S1	S3	S4,S5
PANEL1 (4,2)	Green	G-blinking	Y-blinking	Dark
LPANEL1 (3,5,7)	Green	G-blinking	G-blinking	Dark
LSJ1 (1,2)	Dark	Dark	Yellow	Dark
SJ1 (1,3) (2,3)	Light	Blinking	Blinking	Dark
GLED0	HIGH	HIGH	LOW	HIGH
GLED1	LOW	SWITCH	SWITCH	HIGH

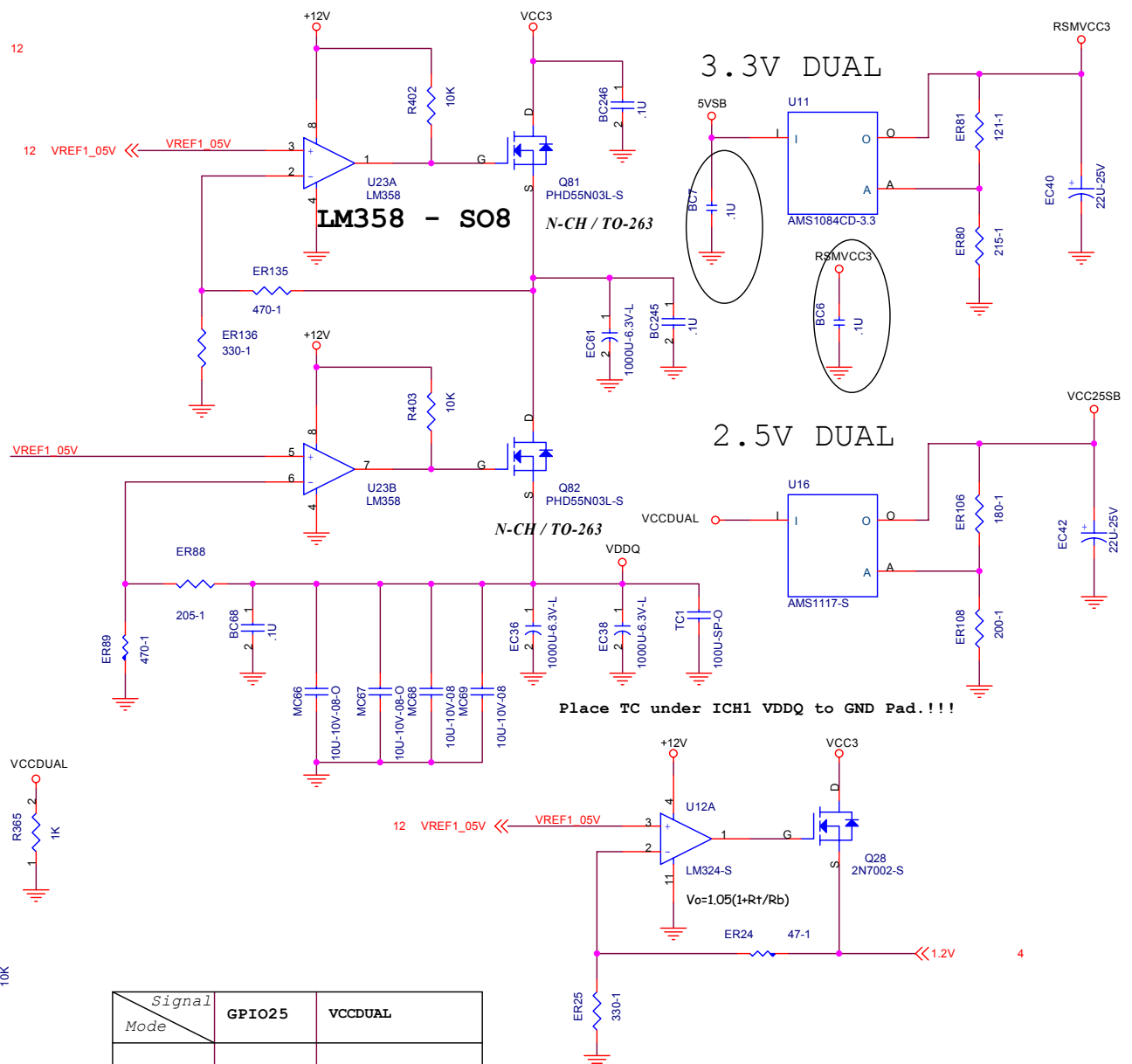


Green Led For Legend

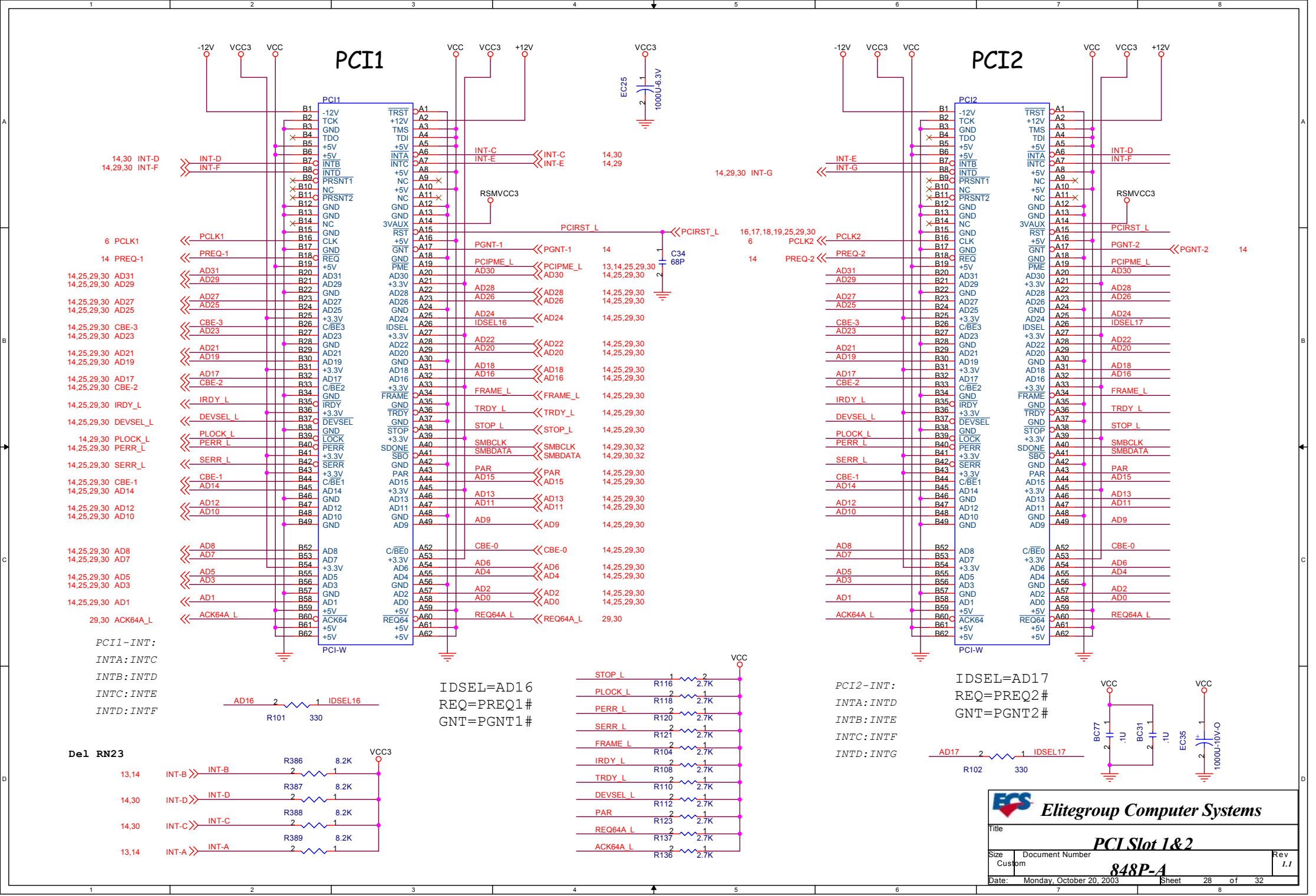


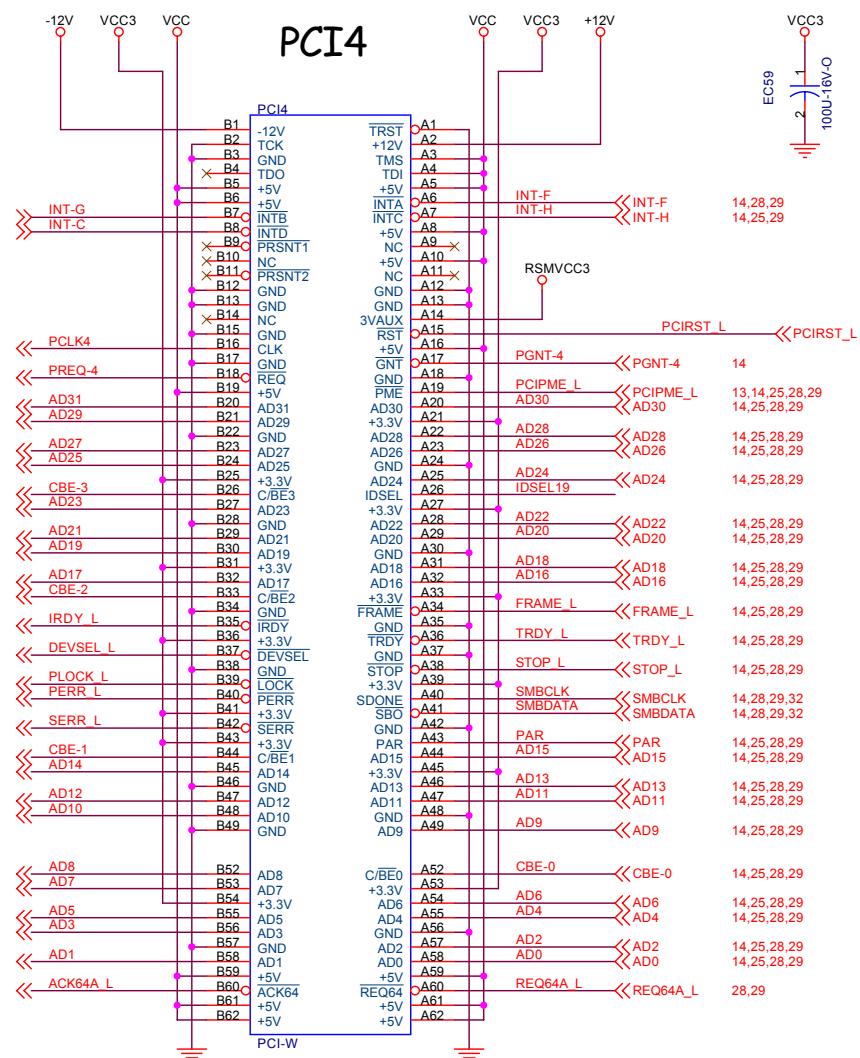


DEL
L12, L36, D22, ER123, D21, Q63, ER87, ER122, BC101

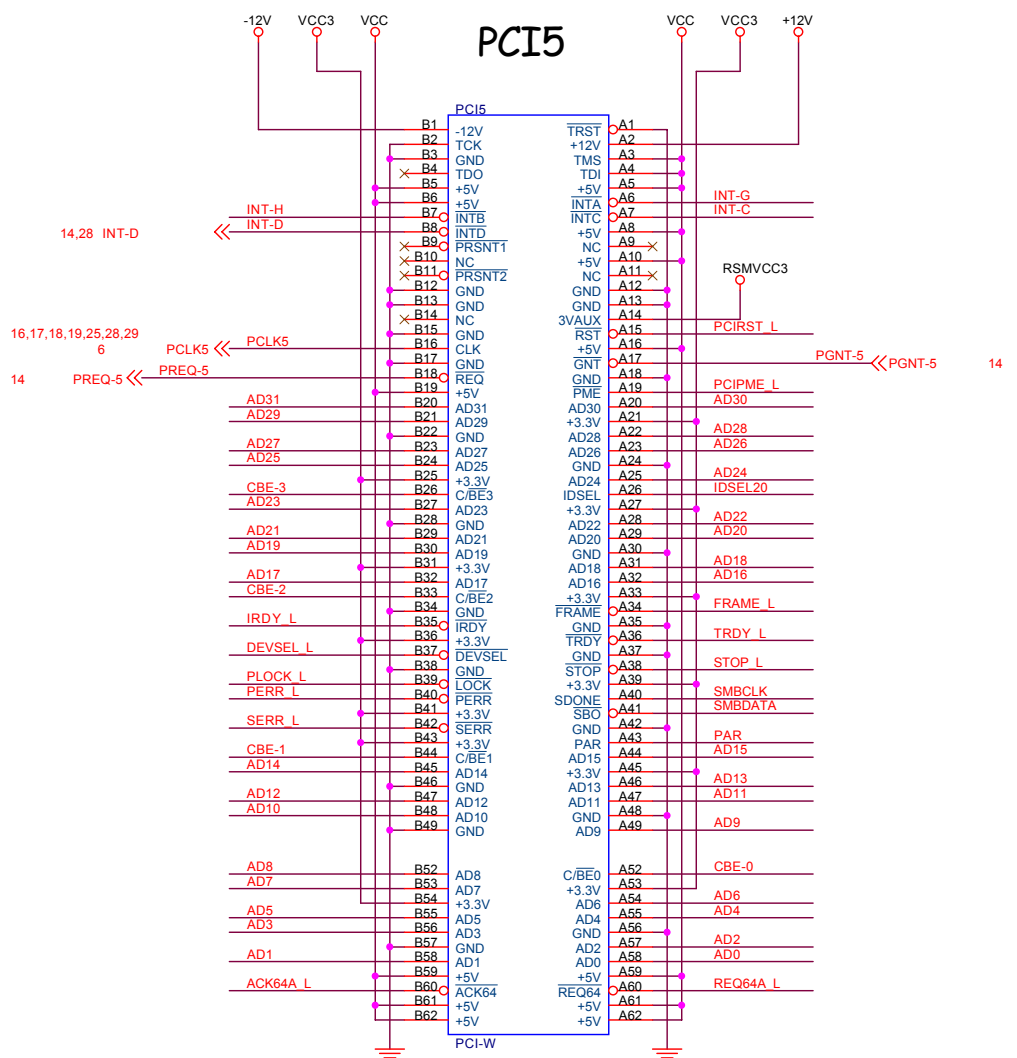
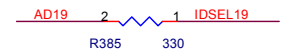


Signal Mode	GPIO25	VCCDUAL
S4/S5	0	0 (No Power)
S4/S5	1	1 (5VSB)

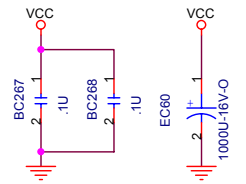
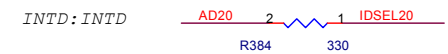


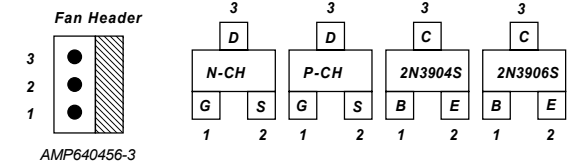
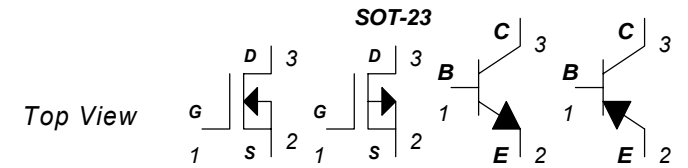


PCI4-INT: IDSEL=AD19
 INTA: INTF REQ=PREQ4#
 INTB: INTG GNT=PGNT4#
 INTC: INTH
 INTD: INTC

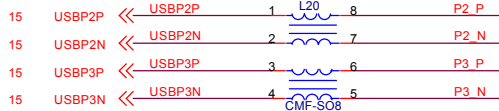
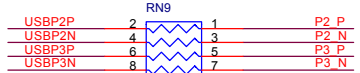


PCI5-INT: IDSEL=AD20
 INTA: INTG REQ=PREQ5#
 INTB: INTH GNT=PGNT5#
 INTC: INTC
 INTD: INTD





REAR_SIDE



REAR_SIDE

